

**PATENT APPLICATION****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: 2511/102

Benyamin HARZANU et al.

Appln. No.: 10/660,356

Group Art Unit: 2858

Examiner: Doyle, Timothy J.

Filed: September 11, 2003

For: METHOD AND APPARATUS FOR THE ELECTRICAL TESTING OF PRINTED CIRCUIT BOARDS EMPLOYING INTERMEDIATE LAYER GROUNDING

SECOND PRELIMINARY AMENDMENT**MAIL STOP NON-FEE AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In order to place this application in better condition for allowance, kindly amend the application as follows:

02/09/2004 TBESHAI1 00000149 194972 10660356

01 FC:1202 90.00 DA